



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: Dagobert M. De LEEUW et al.
SERIAL NO.: 09/817,107 EXAMINER: Kiesha L. Rose
FILED: March 26, 2001 ART UNIT: 2822
FOR: INTEGRATED CIRCUIT PROVIDED WITH A SUBSTRATE
AND WITH A MEMORY, TRANSPONDER, AND METHOD
OF PROGRAMMING A MEMORY

10/16/02
to DeGalan
11-050-
A-Welch

REQUEST FOR APPROVAL OF DRAWING CORRECTION

Assistant Commissioner for Patents
Washington, DC 20231

RECEIVED
NOV 22 2002
TECHNOLOGY CENTER 2800

Dear Sir:

A proposed drawing correction for Fig. 5, marked in red ink, is submitted
herewith for approval, as required by the Office Action.

In addition, Figs. 6A and 6B have been added to show the security paper recited
in claim 11 and disclosed in the specification at page 7, lines 14-23.

Date: November 14, 2002

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By: 
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